

RELATED ART

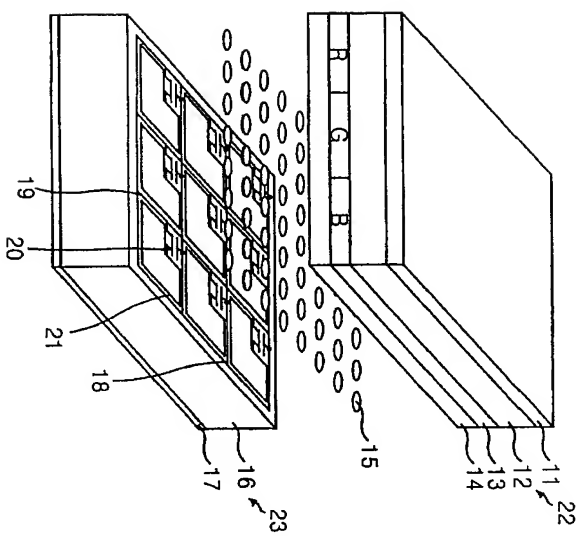


FIG. 2
RELATED ART

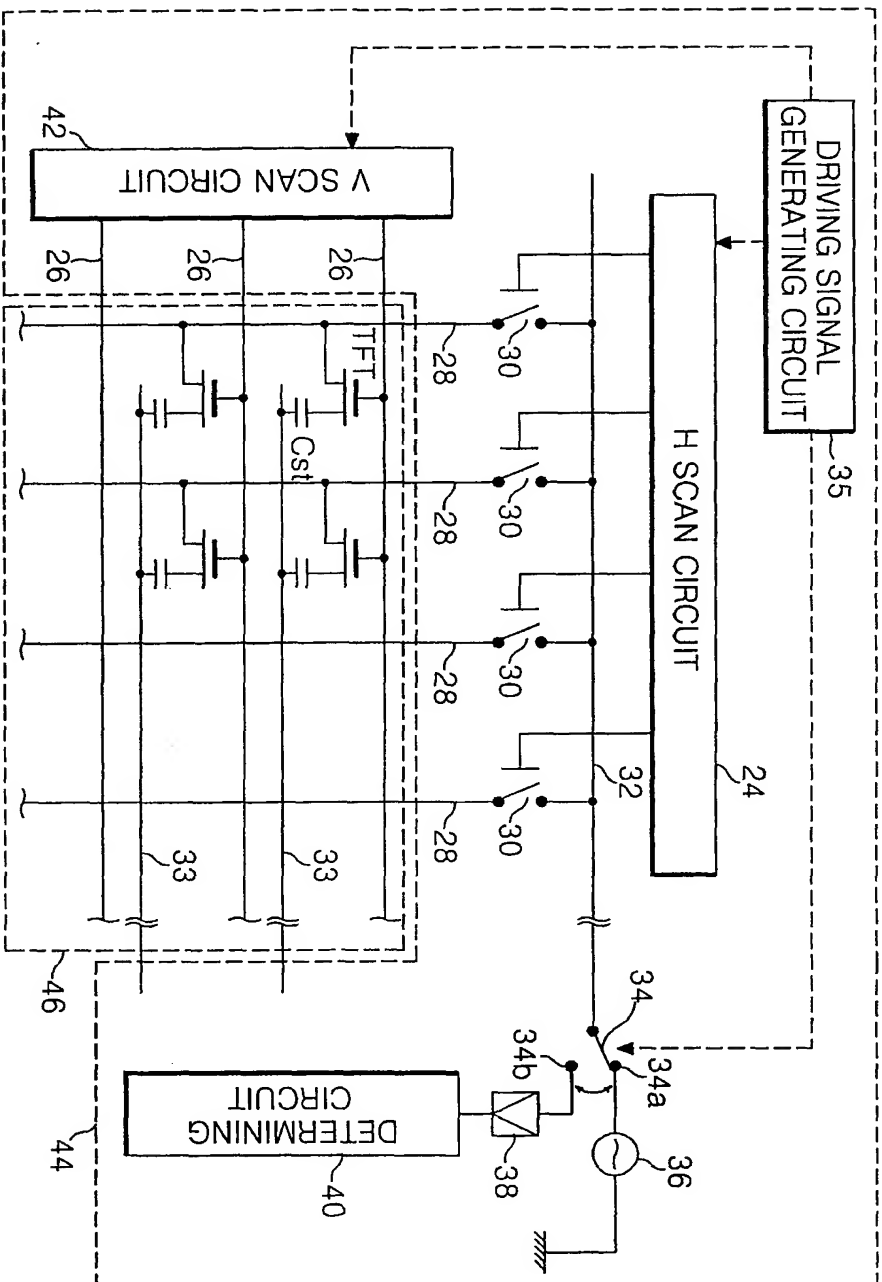


FIG. 3

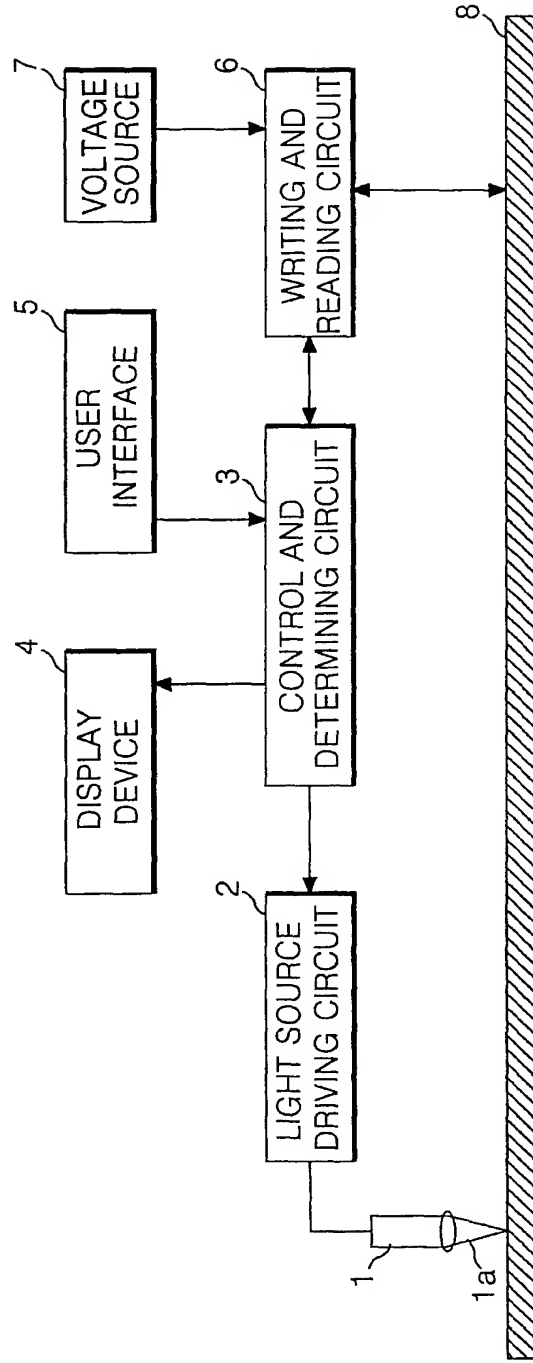


FIG. 4

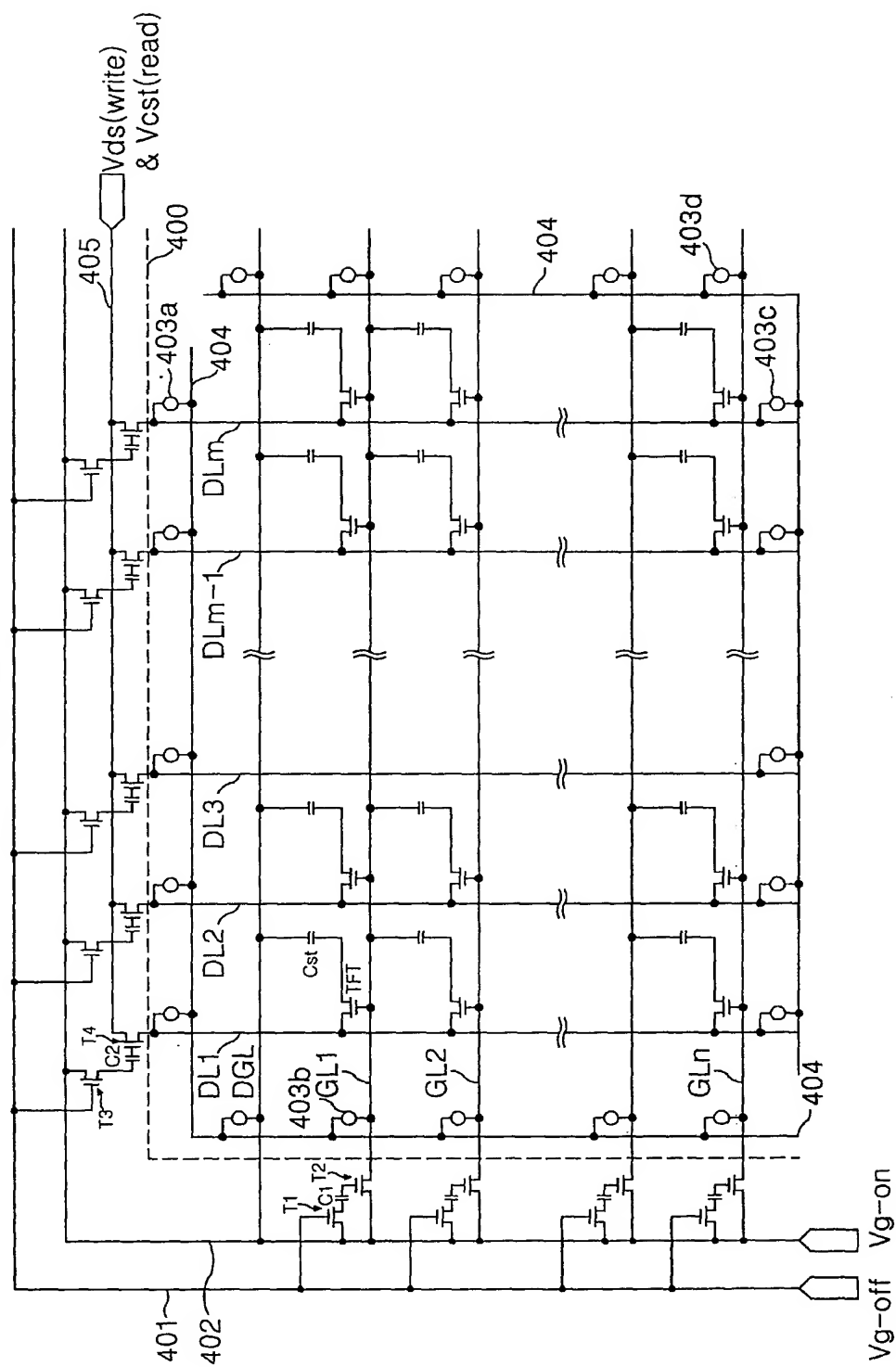


FIG. 5

FIG. 5a shows a 1T1C1T2C2 TFT array. It consists of a grid of TFTs and capacitors. The TFTs are labeled T1, T2, T3, and T4. The capacitors are labeled C1 and C2. The array is connected to a gate voltage source Vg-on and a source voltage source Vg-off. The output of the array is connected to a source follower circuit. The source follower circuit includes a TFT labeled 1a and a capacitor labeled Cst. The output of the source follower is connected to a load resistor labeled Rload. The array is also connected to a data bus labeled Vds(write).

FIG. 5b shows a 1T1C1T2C2 TFT array with a source follower. It is similar to FIG. 5a, but the source follower circuit is replaced by a source follower circuit. The source follower circuit includes a TFT labeled 1a and a capacitor labeled Cst. The output of the source follower is connected to a load resistor labeled Rload. The array is also connected to a data bus labeled Vds(write).

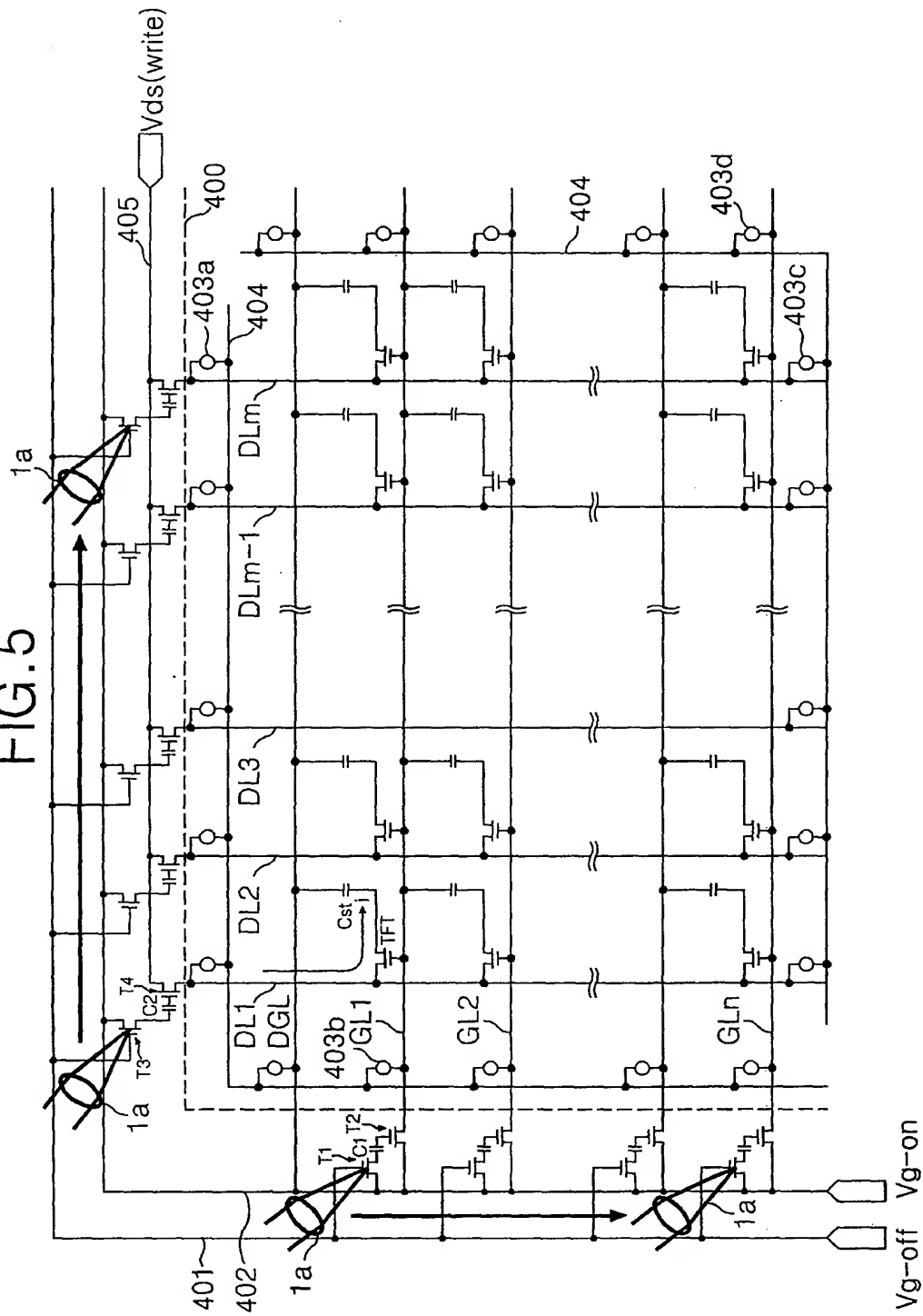


FIG. 6

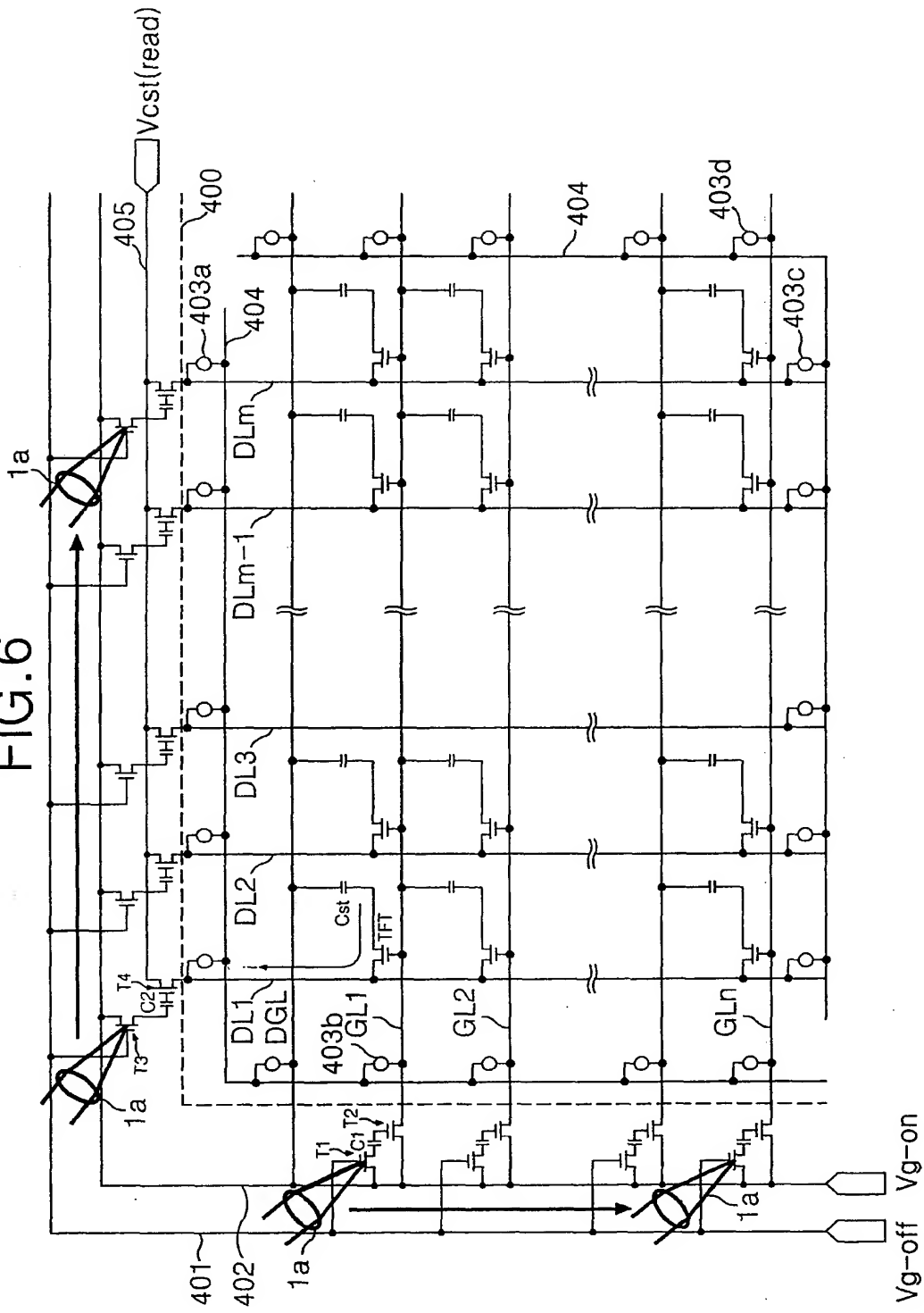


FIG. 7

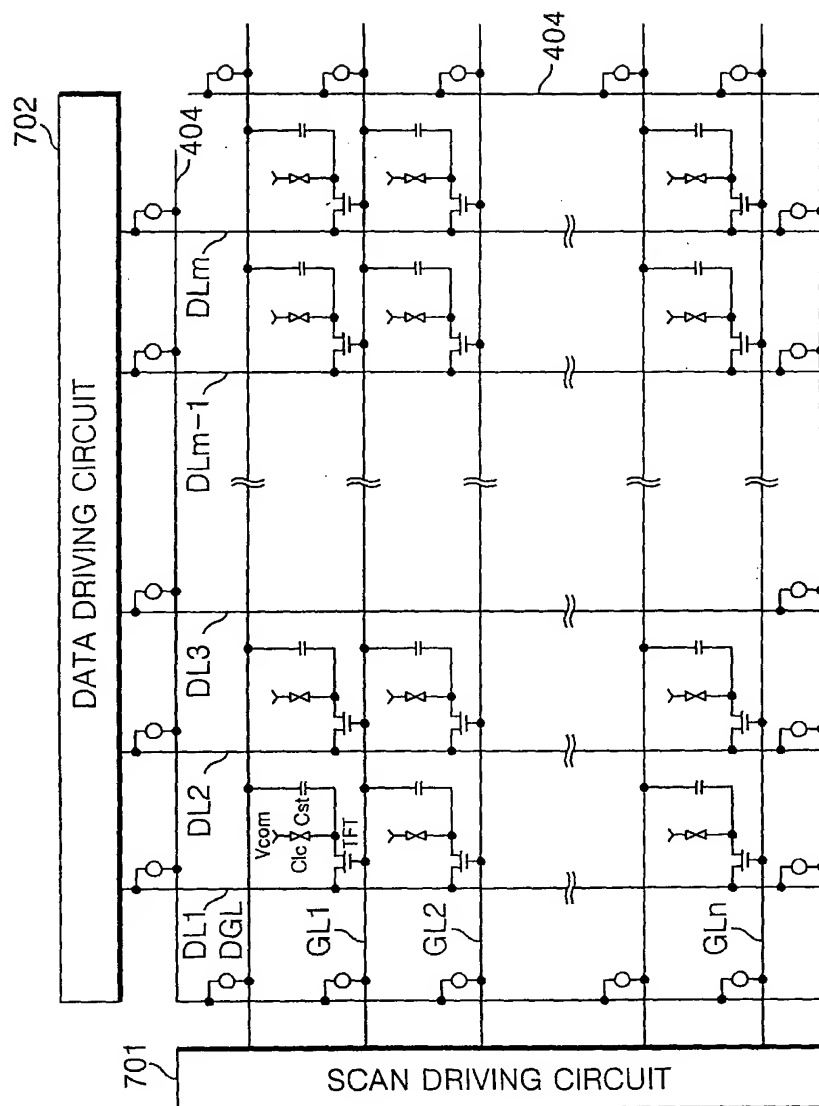


FIG. 8

